

Electronic Acknowledgement Receipt

EFS ID:	5976280
Application Number:	10709041
International Application Number:	
Confirmation Number:	3040
Title of Invention:	LOW TRIGGER VOLTAGE ESD NMOSFET TRIPLE-WELL CMOS DEVICES
First Named Inventor/Applicant Name:	Kiran V. Chatty
Customer Number:	42221
Filer:	Burton A. Amernick/Mary Ann Taylor
Filer Authorized By:	Burton A. Amernick
Attorney Docket Number:	BUR920030188US1
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Application Type:	Utility under 35 USC 111(a)

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Submitted with Payment	yes
Payment Type	Deposit Account
Payment was successfully received in RAM	\$540
RAM confirmation Number	2720
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
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1	Notice of Appeal Filed	Notice_Appeal_08_28_09.pdf	50762 92481649e41c992ae9f8ba01758c44720f9 96ed	no	1
Warnings:					
Information:					
2	Pre-Brief Conference request	Request_Review_08_28_09.pdf	53726 c52f16eeed80a28f9a0f85b59f865c7af8799caf d83	no	1
Warnings:					
Information:					
3	Pre-Brief Conference request	Remarks_08_28_09.pdf	66394 38095b7ef91571bf08b729ea22598bc35e 8544	no	4
Warnings:					
Information:					
4	Fee Worksheet (PTO-875)	fee-info.pdf	30440 7a2d8d7117858fca0f866ef1af1884d0951 311e	no	2
Warnings:					
Information:					
Total Files Size (in bytes):			201322		
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